

**IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE**

In re Application of:	)	Group Art Unit: 2183
Takashi Mita <i>et al.</i>	)	
	)	Examiner: Fennema, Robert E.
Serial No.: 10/500,197	)	
	)	
Filed: June 24, 2004	)	
	)	
For: LOGIC COMPUTING SYSTEM	)	
AND RELATED METHOD	)	
HAVING PRE-LOADED	)	
ON-CHIP CONFIGURATION	)	
DATA	)	

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January 9, 2008

**Submitted via EFS**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

**AMENDMENT UNDER 37 C.F.R. 1.111**

This communication is in response to the non-final Office Action of October 9, 2007. The three-month response deadline expired on January 9, 2008; thus, this Amendment is timely.